

# DELHI TECHNOLOGICAL UNIVERSITY

*Bawana Road, Delhi - 110 042.*

**DATE SHEET: B.TECH. (EVENING) MID SEMESTER EXAMINATIONS (SEPTEMBER 2015)**

**Time: 06.30 p.m to 08.00 p.m.**

## **(ADDENDUM)**

<b>Date &amp; Day</b>	<b>Fifth Semester</b>
<b>21.09.2015 (Monday)</b>	<b>CEIT 501 Compute Networks</b>
<b>22.09.2015 (Tuesday)</b>	<b>CEIT 502 Algorithm Design &amp; Analysis.</b>
<b>23.09.2015 (Wednesday)</b>	<b>CEIT 503 Software Engineering</b>
<b>24.09.2015 (Thursday)</b>	<b>CEIT 504 Theory of Computation</b>

*September, 2015*

***(Prof. Vishal Verma)***  
*Controller of Examinations*

