

**Delhi Technological University**  
(Formerly Delhi College of Engineering)  
Shahbad Daulatpur, Bawana Road, Delhi 110 042

F. No/COE/Misc/2015/

Date: 24.11.15

**ADDENDUM IN DATE SHEET**  
**END SEMESTER EXAMINATIONS NOV-DEC-2015**

The following papers have been scheduled/re-scheduled as per details given below:

**B. Tech**

Paper Code	Subject	Date of Exam
EN-112*	Environmental Sciences	Scheduled on 29.11.15 (Sunday) (10.00 am to 1.00 pm)

**M. Tech**

Paper Code	Subject	Date of Exam (10.00 am to 1.00 pm)
EC-522	SPDD-Digital System Design	Re-Scheduled on 04.12.15 (Saturday) from 26.11.2015 (Thursday)
EC-561	VLSI-Digital System Design	Re-Scheduled on 04.12.15 (Saturday) from 30.11.2015 (Monday)

\*Back paper for students of 2014 and earlier batches (Erstwhile Group B)

**(Prof. Vishal Verma)**  
**Controller of Examination**

Copy to:

1. PS to VC – for information
2. PS to Pro VC – for information
3. Deans (UG & PG)
4. All HODs
5. Registrar
6. Supdt. Of Exam [B. Tech (F/T) & M. Tech]