



DELHI TECHNOLOGICAL UNIVERSITY

Bawana Road, Delhi - 110 042.

DATE SHEET : M.TECH. (Power Electronics Systems) (Part-Time Program) END SEMESTER EXAMINATIONS

F.NO. DTU/Exam./Date Sheet/2013-14/

Date:

In addition to M.Tech. (Power Electronics Systems) (Part-Time Program) Examination Date Sheet, dated 27/12/2013 the following **changes** in respect of M.Tech. (Power Electronics Systems) are being notified.

FIRST SEMESTER

Date	Time	Paper Code	Subject Title
17.01.2014 (Friday)	2:00PM To 5:00PM	EE-521	Modeling & Analysis of Electrical Systems
18.01.2014 (Saturday)	2:00PM To 5:00PM	EE-522	Integrated Electronics & Applied Instrumentation
19.01.2014 (Sunday)	2:00PM To 5:00 PM	EE-523	Power Semiconductor Devices & Magnetics
20.01.2014 (Monday)	6:00PM To 9:00PM	EE-524	Power Electrical Lab

January 15th, 2014

*(O. P. Shukla)
Controller of Examinations*

Copy to:-

- 1. PA to Hon'ble Vice Chancellor, for information.*
- 2. Dr. Ram Singh, Superintendent, M. Tech (Power Electronics System) End Semester Exam.*
- 3. HOD (EE) with a request to inform the concerned students.*
- 4. Asstt.Registrar (Academic)- to issue admit card accordingly*
- 5. OSD (Results), DTU with request to display on website.*