



**Delhi Technological University**  
**(Formerly Delhi College of Engineering)**  
 Shahbad Daultapur, Bawana Road, Delhi – 110 042, India

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Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.) of the University.

**Department of Electrical Engineering**

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Date of Viva –voce examination
1.	2K14/PHDEE/01	Prakash Chittora	प्रकाश चित्तौड़ा	28 <sup>th</sup> November, 2018
Title of Ph.D. Thesis		Analysis and Control of Shunt Compensator in Distribution System	एनालिसिस एंड कंट्रोल ऑफ़ शंट कम्पेन्सेटर इन डिस्ट्रीब्यूशन सिस्टम	

**In-charge (Results)**

**(Controller of Examinations)**

Dated: 3<sup>rd</sup> December, 2018



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**Department of Electronics & Communication Engineering**

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Date of Viva –voce examination
1.	2K13/PHDEC/02	Bharat Choudhary	भारत चौधरी	27 <sup>th</sup> November, 2018
Title of Ph.D. Thesis		Analysis and Design of Current Mode Logic	एनालिसिस एंड डिज़ाइन ऑफ़ करंट मोड लॉजिक	

In-charge (Results)

(Controller of Examinations)

Dated: 3<sup>rd</sup> December, 2018