



# Delhi Technological University

## (Formerly Delhi College of Engineering)

Shahbad Daultapur, Bawana Road, Delhi – 110 042, India

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Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.) of the University.

### Department of Applied Physics

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Date of Viva –voce examination
1.	2K13/PHDAP/02	Jaya Madan	जया मदान	9 <sup>th</sup> March, 2018
Title of Ph.D. Thesis		Simulation and Analysis of Gate All Around Tunnel FET For High Performance Analog and RF Applications	सिमुलेशन एंड एनालिसिस ऑफ़ गेट आल अराउंड टनल एफ़ इ टी फॉर हाई परफॉरमेंस एनालॉग एंड आर एफ़ ऐप्लिकेशन्स	

**In-charge (Results)**

**(Controller of Examinations)**

Dated: 26<sup>th</sup> March, 2018