Delhi Technological University

(Formerly Delhi College of Engineering)

Regular Result Notification

Notification No. THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN DECEMBER, 2020 IS DECLARED AS UNDER: -

Program: Master of Technology

Branch: VLSI Design & Embedded System

TC: Total Credits DT : Detained RL: Result Later RW: Result Withdrawn AB: Absent

VLS5407 : IC Technology VLS501: ANALOG IC DESIGN VLS5307: Organic and Flexible Electronics VLS503: DIGITAL CMOS IC DESIGN

VLS5201 : Seminar

Sr.No	Name	Roll No.	VLS501	VLS5307	VLS503	VLS5407	VLS5201	TC	SGPA	Papers Failed
		Credits	4	3	4	4	2			
1	TARTEEL OSMAN	2K20/VLS/26	В	I	В	Α	B+	14	5.53	VLS5307,

Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC B.tech(Eve.) within 15 days of declaration of result, in the prescribed proforma.

Digitally Signed by Madhukar Cherukuri, OIC (Results)

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Controller Of Examination

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Revised

Sem: I

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