Delhi Technological University

(Formerly Delhi College of Engineering)
Regular Result Notification

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN DECEMBER , 2020 IS DECLARED AS UNDER : -

Notification No.   1181

Program : Master of Technology

Sem : I

Branch :   VLSI Design & Embedded System

TC : Total Credits	DT : Detained	AB : Absent	RL : Result Later	RW : Result Withdrawn

VLS501 : ANALOG IC DESIGN   VLS5307 : Organic and Flexible Electronics   VLS503 : DIGITAL CMOS IC DESIGN   VLS5407 : IC Technology

VLS5201 : Seminar

Sr.No	Name	Roll No.	VLS501	VLS5307	VLS503	VLS5407	VLS5201	TC	SGPA	Papers Failed
		Credits	4	3	4	4	2			
1	JYOTISHMOY BISWAS	2K20/VLS/06	F	F	F	F	F	0	0.00	VLS501VLS5307, VLS503, VLS5407, VLS5201,

Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC B.tech(Eve.) within 15 days of declaration of result , in the prescribed proforma.

Date :   08/02/2021

Digitally Signed by Madhukar Cherukuri , OIC (Results)
valid only if downloaded from http://exam.dtu.ac.in

Controller Of Examination