



Delhi Technological University

(Formerly Delhi College of Engineering)

Regular Result Notification

Visit - <http://exam.dtu.ac.in>

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN NOVEMBER , 2022 IS DECLARED AS UNDER : -

Notification No. 1522

Program : Master of Technology

Sem : I

Branch : VLSI Design & Embedded System

TC : Total Credits

DT : Detained

AB : Absent

RL : Result Later

RW : Result Withdrawn

VLS501 : ANALOG IC DESIGN

VLS503 : DIGITAL CMOS IC DESIGN

VLS5401 : Digital Design With HDL (VERILOG)

VLS6401 : Mixed Signal Design

VLS5307 : Organic and Flexible Electronics

VLS5201 : Seminar

VLS6207 : Selected topics in VLSI

Sr.No	Name	Roll No.	VLS501	VLS503	VLS5401	VLS6401	VLS5307	VLS5201	VLS6207	TC	SGPA	Papers Failed
		Credits	4	4	4	4	3	2	2			
1	NAKKA SRI Krishna CHAITANYA	2K22/VLS/08	A	A+	A	A	A	A+	A	23	8.26	

Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC B.tech(Eve.) within 15 days of declaration of result , in the prescribed proforma.

Date : 30/01/2023

Digitally Signed by Madhukar Cherukuri , OIC (Results)

Controller Of Examination

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