



**Delhi Technological University**  
(Formerly Delhi College of Engineering)  
Shahbad Daulatpur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

**Department of Electrical Engineering**

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K16/PHDEE/09	Ajishkek Raj	अजिषेक राज	Electrical Engineering	09 <sup>th</sup> Sept, 2021	Prof. Pragati Kumar & Prof. D. R. Bhaskar
Title of Ph.D. Thesis		Title in English: Implementation of Signal Processing / Generation Circuits Using Modern Active Building Blocks		Title in Hindi: इम्प्लीमेंटेशन ऑफ़ सिगनल प्रोसेसिंग / जेनेरेशन सर्किट्स यूजिंग मॉडर्न एक्टिव बिल्डिंग ब्लॉक्स		

In-charge (Results)

(Controller of Examinations)

Dated: 13.09.2021