



Delhi Technological University
(Formerly Delhi College of Engineering)
Shahbad Daultapur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.) of the University.

Department of Computer Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Date of Viva – voice examination
1.	2K14/PHDCO/03	ASHISH KUMAR TRIPATHI	आशीष कुमार त्रिपाठी	10 th May, 2019
Title of Ph.D. Thesis		Big Data Analysis Using Metaheuristic Algorithms	बिग डाटा एनालाइसेस यूजिंग मेटाहियूरिस्टिक एल्गोरिदिमस	

In-charge (Results)

(Controller of Examinations)

Dated: 30th May, 2019



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Department of Electrical Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Date of Viva – voice examination
1.	2K16/PHDEE/05	AJAY KUMAR	अजय कुमार	22 nd May, 2019
Title of Ph.D. Thesis		“Analytical Modeling and TCAD Simulation of IN_2O_5SN Transparent Gate Electrode Recessed Channel MOSFET for High Performance Applications”	“एनालिटिकल मॉडलिंग एण्ड TCAD सिमुलेशन ऑफ IN_2O_5SN ट्रान्सपेरेन्ट गेट इलेक्ट्रोड रिसेस्ड चैनल MOSFET फॉर हाई परफॉरमेंस एप्लिकेशनस्”	

In-charge (Results)

(Controller of Examinations)

Dated: 30th May, 2019