



Delhi Technological University
 (Formerly Delhi College of Engineering)
 Shahbad Daulatpur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

Department of Applied Physics

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K18/PHDAP/31	Mekonnen Getnet Yirak	मेकोननेन गेटनेट यिराक	Applied Physics	19 th September, 2023	Prof. Rishu Chaujar
Title of Ph.D. Thesis		Title in English: TCAD Analysis And Modeling of Gate Stack Gate All Around Junction Less Nanowire Field Effect Transistor for Sensing Applications		Title in Hindi: TCAD एनालिसिस एंड मॉडलिंग ऑफ गेट स्टैक गेट ऑल एराउंड जंक्शन लैस नैनोवायर फिल्ड इफेक्ट ट्रांजिस्टर फॉर सेंसिंग एप्लिकेशन्स		

In-charge (Results)
Dated: 26.09.2023

(Controller of Examinations)