



Delhi Technological University
(Formerly Delhi College of Engineering)

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN NOV 2023 IS DECLARED AS UNDER:-

Master of Technology(VLSI Design & Embedded System), I-SEMESTER

Result Declaration Date : 02-02-2024

Notification No. : 1652

VLS5401 : Digital Design With HDL (VERILOG)

Sr.No	Roll No.	Name of Student	VLS5401	SGPA	TC	CGPA	Failed Courses
			4.00				
1	23/VLS/22	ANKIT TIWARI	NA	

VLS5401 : Digital Design With HDL (VERILOG) VLS5307 : Organic and Flexible Electronics VLS5201 : Seminar VLS503 : DIGITAL CMOS IC DESIGN VLS501 : ANALOG IC DESIGN

Sr.No	Roll No.	Name of Student	VLS5401	VLS5307	VLS5201	VLS503	VLS501	SGPA	TC	CGPA	Failed Courses
			4.00	3.00	2.00	4.00	4.00				
2	23/VLS/25	YASHODA PARMAR	A+	A+	O	O	A	9.12	17	9.118	

VLS5401 : Digital Design With HDL (VERILOG) VLS5307 : Organic and Flexible Electronics VLS6305 : Machine Learning VLS6207 : Selected topics in VLSI VLS5201 : Seminar VLS503 : DIGITAL CMOS IC DESIGN VLS501 : ANALOG IC DESIGN

Sr.No	Roll No.	Name of Student	VLS5401	VLS5307	VLS6305	VLS6207	VLS5201	VLS503	VLS501	SGPA	TC	CGPA	Failed Courses
			4.00	3.00	3.00	2.00	2.00	4.00	4.00				
3	23/VLS/01	BRIJ NANDAN SINGH	O	A+	B+	A+	A+	A+	O	9.42	22	9.421	
4	23/VLS/03	SHASHI RANJAN UPADHYAY	A+	A	A	A+	A	A+	B+	8.32	22	8.316	
5	23/VLS/04	PRAKHAR SAINI	A	B	B+	A	A+	A+	B+	7.79	22	7.789	
6	23/VLS/05	MOHAMMAD TALHA	A	A	B	A	A	B+	B	7.37	22	7.368	
7	23/VLS/06	PRAJVI UDAR	A+	A	C	O	O	A	A	8.63	22	8.632	
8	23/VLS/07	MAYANK CHAUHAN	A+	B+	B	B+	A+	A	B+	7.84	22	7.842	
9	23/VLS/08	AMARTYA ROY	B+	B+	B+	C	A+	A	A	7.42	22	7.421	

Paadhuks

OIC (Results)

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Controller of Examination

Note: Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC(Results) within 15 days of declaration of result in the prescribed proforma.



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Sr.No	Roll No.	Name of Student	Grade 1	Grade 2	Grade 3	Grade 4	Grade 5	Grade 6	Grade 7	Grade 8	CGPA	TC	CGPA
10	23/VLS/09	DEEPTI	A	B+	B	A	A+	B+	B+	7.53	22	7.526	
11	23/VLS/10	AASTHA SINGH	A	A	A+	O	A+	A+	A	8.53	22	8.526	
12	23/VLS/11	GAJJAR JAY UMESH BHAI	A	B+	C	B+	O	A+	A+	8.37	22	8.368	
13	23/VLS/12	PREETI	B+	B+	A+	A	A+	A	B	7.32	22	7.316	
14	23/VLS/13	ASMAR HAFEEZ	B+	B	P	A	A	A	B+	7.26	22	7.263	
15	23/VLS/14	TUSHANT VERMA	A+	B	B+	A	A+	B+	B+	7.58	22	7.579	
16	23/VLS/15	AKHILESH VISHWAKARMA	A	A	B+	A+	A+	B+	B+	7.79	22	7.789	
17	23/VLS/16	RAMLAKHAN	A	A	B+	A+	A+	A	A	8.21	22	8.211	
18	23/VLS/17	DEVESH SAINI	A	B+	B+	B+	A	B+	B+	7.32	22	7.316	
19	23/VLS/18	KUNCHA AKHILESH KUMAR	B+	B	B+	C	A+	B	B	6.42	22	6.421	
20	23/VLS/19	SOUMYAJIT DAS	A+	B+	B+	B	O	A	B+	7.84	22	7.842	
21	23/VLS/20	UJJWAL BHARTI	A	B+	B+	B+	A+	A	B	7.42	22	7.421	
22	23/VLS/21	TVISHA PUSHKAR	O	O	B	A+	A+	A+	O	9.58	22	9.579	
23	23/VLS/23	MANISH SINGH	B+	B+	B+	C	A+	A	B+	7.21	22	7.211	
24	23/VLS/24	ADITYA RAYER	A+	B	B	B+	A+	A	B	7.47	22	7.474	

VLS6207 : Selected topics in VLSI

Sr.No	Roll No.	Name of Student	VLS6207 2.00	SGPA	TC	CGPA	Failed Courses
25	2K22/VLS/10	RAVINDER KAUR	A	8	0	8	

TC - Total Secured Credits DT - Detained AB - Absent RL - Result Later RW - Result Withdrawn

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OIC (Results)

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Controller of Examination

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