



DELHI TECHNOLOGICAL UNIVERSITY

(Formerly Delhi College of Engineering)
Shahabad Daulatpur, Bawana Road, Delhi-110042


DTU/2K18/PHDEC/514/9274

Dated: 09.11.2024

CIRCULAR

The University has scheduled the Ph.D. Viva Voce examination on 12th November, 2024 (Tuesday) at 02:00 p.m. at the Committee Room, Department of Electronics & Communication Engineering in r/o Ms. Neetika Yadav, Roll No. 2K18/PHDEC/514, Department of Electronics & Communication Engineering on her Ph.D. research work thesis titled "STUDY OF ALTERNATIVE LOGIC STYLES SUITABLE FOR LOW POWER VLSI DESIGN."

All the concerned are requested to take further necessary action in this regard.



(Prof. Rajeshwari Pandey)
Controller of Examination

DTU/2K18/PHDEC/514/9274

Dated: 09.11.2024

Copy to the:

1. PS to VC, for kind information to Hon'ble Vice-Chancellor, DTU
2. Dean, Academic-PG
3. Associate Dean-PG (Thesis submission Date: 31.12.2023)
4. HOD, Department of Electronics & Communication Engineering
5. The DRC Chairperson, Department of Electronics & Communication Engineering
6. All HoD
7. Prof. Neeta Pandey & Dr. Deva Nand, Department of Electronics & Communication Engineering & Supervisor(s) of student concerned with **the request to make necessary arrangements for Ph.D. Viva Voce.**
8. Incharge, Result Section with the request to upload the circular on Examination Branch website.
9. The Candidate- Ms. Neetika Yadav, Roll No. 2K18/PHDEC/514 **through the Supervisor(s) concerned.**
10. Guard File.


(Prof. Rajeshwari Pandey)
Controller of Examination