



Delhi Technological University

(Formerly Delhi College of Engineering)
Shahbad Daultapur, Bawana Road, Delhi – 110 042, India

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Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.) of the University.

Department of Computer Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Date of Viva –voce examination
1.	2K11/PHDCO/09	Rajesh Kumar Yadav	राजेश कुमार यादव	24 th November, 2018
Title of Ph.D. Thesis		Data Aggregation and Fusion Models for Wireless Sensor Network	डाटा एग्रीगेशन एंड फ्यूजन मॉडल्स फॉर वायरलेस सेंसर नेटवर्क	

In-charge (Results)

(Controller of Examinations)

Dated: 30th November, 2018



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Department of Electronics & Communication Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Date of Viva –voce examination
1.	2K11/PHDEC/07	Deva Nand	देवा नन्द	19 th November, 2018
Title of Ph.D. Thesis		Design and Implementation of Analog Circuits using Current Mode Building Blocks	डिज़ाइन एंड इम्प्लीमेंटेशन ऑफ़ एनालॉग सर्किट्स युजिंग करंट मोड बिल्डिंग ब्लॉक्स	

In-charge (Results)

(Controller of Examinations)

Dated: 30th November, 2018