

# Delhi Technological University

(Formerly Delhi College of Engineering)

Shahbad Daulatpur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

## Department of Electronics and Communication Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K14/PHDEC/01	Saji. M. Antony	साजी. एम. एंटोनी	Electronics and Communication Engineering	14 <sup>th</sup> July, 2020	Prof. S. Indu & Prof. Rajeshwari Pandey
<b>Title of Ph.D. Thesis</b>		<b>Title in English:</b> Design of Energy Efficient Transceiver Blocks For Wireless Sensor Nodes		<b>Title in Hindi :</b> डिज़ाइन ऑफ़ एनर्जी एफ़िफ़िसिएंट ट्रान्सीवर ब्लॉक्स फॉर वायरलेस सेंसर नोड्स		

In-charge (Results)

  
(Controller of Examinations)

Dated: 21.07.2020

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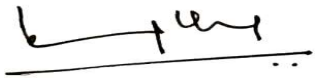
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## Department of Electronics and Communication Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K14/PHDEC/10	Priyanka Gupta	प्रियंका गुप्ता	Electronics and Communication Engineering	13 <sup>th</sup> July, 2020	Prof. Rajeshwari Pandey
Title of Ph.D. Thesis		Title in English: Design of Continuous Time Circuits using Current Mode Building Blocks		Title in Hindi: डिज़ाइन ऑफ़ कंटीन्यूअस टाइम सर्किट्स यूजिंग करंट मोड बिल्डिंग ब्लॉक्स		

In-charge (Results)

  
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Dated: 21.07.2020