



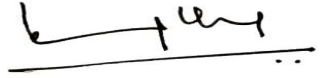
Delhi Technological University
 (Formerly Delhi College of Engineering)
 Shahbad Daultpur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

Department of Electrical Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K11/PHDEE/15	Ram Bhagat	राम भगत	Electrical Engineering	23rd November, 2020	Prof. Pragati Kumar & Prof. D.R. Bhaskar
Title of Ph.D. Thesis		Title in English: Realization of a Class of Analog Signal Processing and Generation Circuits Employing Current Differencing Buffered Amplifiers		Title in Hindi: रियलाईज़ेशन ऑफ़ ए क्लास ऑफ़ एनालॉग सिग्नल प्रोसेसिंग एंड जनरेशन सर्किट्स एम्प्लॉयिंग करंट डिफ्रेंसिंग बफर्ड एम्प्लीफायर		

In-charge (Results)


 (Controller of Examinations)

Dated: 27.11.2020