



Delhi Technological University
 (Formerly Delhi College of Engineering)
 Shahbad Daulatpur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

Department of Electronics and Communication Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K17/PHDEC/03	Munindra	मुनिन्द्र	Electronics and Communication Engineering	22 nd June, 2023	Dr. Deva Nand
Title of Ph.D. Thesis		Title in English: Study and Modeling of Single and Multi Layer Graphene Field Effect Transistor Structures With Various Dielectrics		Title in Hindi: स्टडी एंड मॉडलिंग ऑफ सिंगल एंड मल्टी लेयर ग्राफिन फिल्ड एफेक्ट ट्रांसिस्टर स्ट्रक्चरस विद वेरियस डाइलैक्ट्रिक्स		

(Signature)

In-charge (Results)
Dated: 28.06.2023

(Signature)

(Controller of Examinations)