Delhi Technological University

(Formerly Delhi College of Engineering)

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN MAY 2024 IS DECLARED AS UNDER:-

Master of Technology(VLSI Design & Embedded System), IV-SEMESTER

Result Declaration Date : 07-08-2024   **Notification No: 1704**

VLS602 : MAJOR PROJECT II

Sr.No	Roll No.	Name of Student	VLS602 12.00	SGPA	TC	Failed Courses
1	2K22/VLS/01	ANKIT RAJ	B+	7	12	
2	2K22/VLS/02	BHARGAV SINGH	B	6	12	
3	2K22/VLS/03	BHUVNESH SINGH	B+	7	12	
4	2K22/VLS/04	DEAVERCHIT SINGH BAGHEL	B+	7	12	
5	2K22/VLS/05	FARHEEN SAFDAR	B+	7	12	
6	2K22/VLS/06	KUNAL RUPRAJ JAGTAP	B	6	12	
7	2K22/VLS/07	MAYANK KAUSHIK	P	4	12	
8	2K22/VLS/08	NAKKA SRI KRISHNA CHAITANYA	B+	7	12	
9	2K22/VLS/09	NISHANT SHARMA	B+	7	12	
10	2K22/VLS/10	RAVINDER KAUR	C	5	12	
11	2K22/VLS/11	RICHA DUBEY	B+	7	12	
12	2K22/VLS/12	RUPESH KUMAR	B+	7	12	
13	2K22/VLS/14	SAGAR GOSAI	B+	7	12	
14	2K22/VLS/15	SHALU KESHARI	B	6	12	
15	2K22/VLS/16	SHIV SHANKAR YADAV	A+	9	12	
16	2K22/VLS/17	SHUBHAM AGGARWAL	C	5	12	
17	2K22/VLS/18	SHUBHAM GUPTA	B	6	12	
18	2K22/VLS/19	SUNRIKA	B+	7	12	
19	2K22/VLS/20	SURAJ GUPTA	B+	7	12	
20	2K22/VLS/21	SURAJ KUMAR	C	5	12	

OIC (Results)   **Controller of Examination**

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<div align="right">

Notification No: 1704

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21	2K22/VLS/22	TARUN GUPTA	B+	7	12	
22	2K22/VLS/23	VIPIN KUMAR SHARMA	B+	7	12	

OIC (Results)

Controller of Examination

Note: Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC(Results) within 15 days of declaration of result in the prescribed proforma.