



Delhi Technological University
(Formerly Delhi College of Engineering)
Shahbad Daulatpur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

Department of Electronics & Communication Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K19/PHDEC/19	Sumedha Gupta	सुमेधा गुप्ता	Electronics & Communication Engineering	24 th September, 2024	Prof. Neeta Pandey & Prof. R.S. Gupta, MAIT, Delhi
Title of Ph.D. Thesis		Title in English: Analytical Modelling, Simulation and Characterization of Junctionless Accumulation Mode Mosfet for Analog and RF Circuit Applications		Title in Hindi: एनालिटिकल मॉडलिंग, सिमुलेशन एंड कैरेक्टराइजेशन ऑफ जंक्शनलेस एक्क्यूमुलेशन मोड मॉस्फेट फॉर एनालॉग एंड RF सर्किट एप्लिकेशन्स		

Radhika

In-charge (Results)
Dated: 16.10.2024

R. Pandey

Controller of Examination