Delhi Technological University
(Formerly Delhi College of Engineering)
Consolidated Result Notification

Programme : Master of Technology

> The following Candidates who appeared in Master of Technology-Signal Processing & Digital Design End Semester examinations held in MAY, 2025 have completed the requirement and hence declared to have pass the final examinations. These Candidates have obtained 58 or more credits.

Branch : Signal Processing & Digital Design

Notification Number : 1861

Result Declaration Date : 2025-07-14

Sr. No.	Name	Roll No.	Name in Hindi	TC	CGPA
	CGPA - Cumulative Grade Point Average		TC - Total Credits		
1	ASHUTOSH DUBEY	23/SPD/01	आशुतोष दुबे	58	7.84
2	SUYASH GUPTA	23/SPD/03	सुयश गुप्ता	58	7.50
3	VAIBHAV SHARMA	23/SPD/04	वैभव शर्मा	58	7.03
4	HARIS SERAJ KHAN	2K22/SPD/04	हारिस सेराज खान	58	7.55
5	KURVA RAJENDER	23/SPD/05	कुर्वा राजेंदर	58	8.16
6	RITIKA GUPTA	23/SPD/06	रितिका गुप्ता	58	8.90
7	ARPIT SHARMA	23/SPD/07	अर्पित शर्मा	58	8.40
8	ASHISH KARAIYA	23/SPD/09	आशीष करैया	58	7.41
9	POTTURI SHRAVYA PRAVALLIKA	23/SPD/10	पोत्तुरि श्रव्य प्रवल्लिका	58	7.60
10	MOHIT GARG	23/SPD/11	मोहित गर्ग	58	7.62
11	ANANYA VERMA	23/SPD/12	अनन्या वर्मा	58	8.81
12	JAINATH GOPE	23/SPD/13	जयनाथ गोप	58	7.69
13	RISHABH SINGH	23/SPD/14	ऋषभ सिंह	58	7.31

OIC (Results)

Controller of Examination

Note: Any discrepancy in the result in rio name/roll no/registration/marks/grades/course code title should be brought to the notice of Controller of Examination/OIC(Results) within 15 days of declaration of result in the prescribed proforma.