



Delhi Technological University
(Formerly Delhi College of Engineering)

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN MAY 2025 IS DECLARED AS UNDER:-

Master of Technology(ELECTRONICS ENGINEERING), II-SEMESTER

Result Declaration Date : 31-Dec-2025

Notification No: 1912

MOC504n : Microwave Active Circuits VLS504n : Mixed Signal Design VLS5323n : Advanced Computer Architecture								
Sr.No	Roll No.	Name of Student	MOC504n	VLS504n	VLS5323n	SGPA	TC	Failed Courses
1	24/REC/01	SUYASH TILAK	4.00	4.00	4.00	7.33	12	
			B+	B	A+			

OIC (Results)

Controller of Examination

Note:Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC(Results) within 15 days of declaration of result, in the prescribed proforma.