



Delhi Technological University
(Formerly Delhi College of Engineering)

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN NOV 2024 IS DECLARED AS UNDER:-

Master of Technology(ELECTRONICS ENGINEERING), I-SEMESTER

Result Declaration Date : 31-Dec-2025

Notification No: 1912

| MOC509 : RF and Microwave Circuits RUC501 : Research and Publication Ethics (RPE) RUC503 : Research Methodology VLS501 : ANALOG IC DESIGN | | | | | | | | | |
|---|-----------|-----------------|--------|--------|--------|--------|------|----|----------------|
| Sr.No | Roll No. | Name of Student | MOC509 | RUC501 | RUC503 | VLS501 | SGPA | TC | Failed Courses |
| 1 | 24/REC/01 | SUYASH TILAK | 4.00 | 2.00 | 4.00 | 4.00 | 6.71 | 14 | |
| | | | B | A+ | B+ | B | | | |

OIC (Results)

Controller of Examination

Note:Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC(Results) within 15 days of declaration of result, in the prescribed proforma.