



**Delhi Technological University**  
(Formerly Delhi College of Engineering)  
Shahbad Daulatpur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

**Department of Applied Physics**

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K19/PHDAP/505	Yash Pathak	यश पाठक	Physics	16 <sup>th</sup> January, 2025	Prof. Rishu Chaujar & Prof. Bansi Dhar Malhotra
<b>Title of Ph.D. Thesis</b>		<b>Title in English:</b> Studies on Technology Computer Aided Design of Double Metal Negative Capacitance FET using DFT and Machine Learning Approach for Analog/Sensing Application		<b>Title in Hindi:</b> स्टडीज ऑन टेक्नोलॉजी कंप्यूटर एडेड डिज़ाइन ऑफ़ डबल मेटल नेगेटिव कैपेसिटेंस FET यूज़िंग DFT एंड मशीन लर्निंग अप्रोचेस फॉर एनालॉग/सेंसिंग एप्लीकेशंस		

*Pradhum*

**In-charge (Results)**

Dated: 30.01.2025

*R. Pandey*

**Controller of Examination**