



Delhi Technological University

(Formerly Delhi College of Engineering)

Shahbad Daultatpur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

Department Of Electronics & Communication Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K21/PHDEC/502	Lokesh Soni	लोकेश सोनी	Electronics & Communication Engineering	07 th November, 2025	Prof. Neeta Pandey
Title of Ph.D Thesis		Title in English: High Performance SRAM Design		Title in Hindi: हाई परफॉर्मेंस एसऱैम डिज़ाइन		

In-charge (Results)

Dated: 07.11.2025

Controller of Examination