



Delhi Technological University
(Formerly Delhi College of Engineering)

THE RESULT OF THE CANDIDATE WHO APPEARED IN THE FOLLOWING EXAMINATION HELD IN MAY 2026 IS DECLARED AS UNDER:-

Master of Technology(Signal Processing & Digital Design), II-SEMESTER

Result Declaration Date : 21-Jul-2026

Notification No: 1968 (Revised on 12.08.2026)

SPD502n : DSP Algorithms and Architectures SPD504n : Detection & Estimation Theory SPD526n : Deep Learning SPD530n : Advanced Computer Architecture SPD546n : Programming/Industrial Training UCC502n : Research Methodology											
Sr.No	Roll No.	Name of Student	SPD502n	SPD504n	SPD526n	SPD530n	SPD546n	UCC502n	SGPA	TC	Failed Courses
			4.00	4.00	4.00	4.00	4.00	4.00			
1	25/SPD/16	SARTHAK	A	B	C	O	B+	B+	7.17	24	
2	25/SPD/17	AKSHAY KUMAR CHAHAL	A	B	B	A	A+	B+	7.33	24	
3	25/SPD/18	ANSHIKA VERMA	A+	B+	A	O	A+	A+	8.67	24	
4	25/SPD/19	ROOPAL SARASWAT	A+	B+	B	A+	O	A	8.17	24	
5	25/SPD/20	ROHIT KUMAR	A	B+	A	O	A	A	8.17	24	
6	25/SPD/21	NISHA AGRAWAL	A+	A+	A	A+	O	A	8.83	24	

OIC (Results)

Controller of Examination

Note:Any discrepancy in the result in r/o name/roll no/registration/marks/grades/course code/title should be brought to the notice of Controller of Examination/OIC(Results) within 15 days of declaration of result, in the prescribed proforma.