



Delhi Technological University

(Formerly Delhi College of Engineering)

Shahbad Daulatpur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

Department of Electronics & Communication Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K20/PHDEC/508	Hemanshi Chugh	हेमांशी चुग	Electronics & Communication Engineering	27 th July, 2026	Dr. Sonal Singh
Title of Ph.D. Thesis		Title in English: DESIGN AND PERFORMANCE OPTIMIZATION OF NANO SCALE VEDIC MULTIPLIER ARCHITECTURE		Title in Hindi: डिजाइन एंड परफॉर्मेंस ऑप्टिमाइजेशन ऑफ नैनो स्केल वैदिक मल्टीप्लायर आर्किटेक्चर		

OSD (Examination)

Dated:04.08.2026

Controller of Examination



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Department of Applied Physics

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K19/PHDAP/10	Ashok	अशोक	Physics	28 th July, 2026	Prof. Vinod Singh & Prof. Rajendra S. Dhaka
Title of Ph.D. Thesis		Title in English: Physical Properties of Transition Metal Substituted Orthovanadates		Title in Hindi: फिजिकल प्रॉपर्टीज़ ऑफ़ ट्रांज़िशन मेटल सब्स्टिट्यूटेड ऑर्थोवैनडेट्स		

OSD (Examination)
Dated:04.08.2026

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