



Delhi Technological University

(Formerly Delhi College of Engineering)
Shahbad Daulatpur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

Department of Applied Physics

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K20/PHDAP/502	Anshul	अंशुल	Physics	31 st August, 2026	Prof. Rishu Chaujar
Title of Ph.D. Thesis		Title in English: SEMI - EMPIRICAL DFT - BASED ATOMIC SCALE MODELING OF GATE-STACK ARMCHAIR GRAPHENE NANORIBBON FET FOR PERFORMANCE ENHANCEMENT AND SENSOR APPLICATION		Title in Hindi: सेमी - एम्पिरिकल डीएफटी - बेस्ड एटॉमिक स्केल मॉडलिंग ऑफ गेट - स्टैक आर्मचेयर ग्राफीन नैनोरिबन एफईटी फॉर परफॉर्मेंस इन्हांसमेंट एंड सेंसर एप्लीकेशन		


 Controller of Examination

Dated:07.09.2026



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Department of Electrical Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K20/PHDEE/505	Md. Modassir Masoom	मो० मोदस्सिर मासूम	Electrical Engineering	27 th August, 2026	Prof. Narendra Kumar II & Dr. Mayank Kumar
Title of Ph.D. Thesis		Title in English: Fault-Tolerant Design and Robust Control of Multiport DC-DC Converters with Reduced Current Ripple for Standalone PV-BES Systems		Title in Hindi: फॉल्ट-टॉलरेंट डिज़ाइन एंड रोबस्ट कंट्रोल ऑफ मल्टीपोर्ट डीसी-डीसी कन्वर्टरस विद रिड्यूस्ड करंट रिपल फॉर स्टैंडअलोन पीवी-बीईएस सिस्टम्स		


Controller of Examination

Dated:07.09.2026