



**Delhi Technological University**  
(Formerly Delhi College of Engineering)  
Shahbad Daultapur, Bawana Road, Delhi – 110 042, India

Consequent upon the evaluation of the thesis submitted by the candidate(s) as per details given below for the award of Doctor of Philosophy (Ph.D.) of the Delhi Technological University on the topic as mentioned against his/her name and after his/her viva-voce examinations, he/she/they has/have been found qualified for the Award of Degree of Doctor of Philosophy (Ph.D.).

Department of Electrical Engineering

S.No.	Roll. No.	Name of Student	Name of Student (In Hindi)	Discipline	Date of Viva – Voce Examination	Name of Supervisor/Co-Supervisor(s)
1.	2K19/PHDEE/505	Chandan Kumar	चन्दन कुमार	Electrical Engineering	20 <sup>th</sup> February, 2026	Dr. Prakash Chittora
Title of Ph.D. Thesis		Title in English: DESIGN AND DEVELOPMENT OF SECURITY FRAMEWORK FOR SMART GRID INFRASTRUCTURE		Title in Hindi: डिज़ाइन एंड डेवलपमेंट ऑफ़ सिक््योरिटी फ्रेमवर्क फॉर स्मार्ट ग्रिड इंफ्रास्ट्रक्चर		

*Radhika*

In-charge (Results)

Dated: 25.02.2026

*Randey*

Controller of Examination